SFP28-BIDI-xx-30

25Gb/s SFP28 BIDI 30km Transceiver

Product Features

- Supports 24.3Gb/s to 26.5Gb/s bit rates
- Up to 30km transmission on SMF
- 1270nm DFB laser and APD receiver for SFP28-BIDI-12-30
- 1330nm DFB laser and APD receiver for SFP28-BIDI-13-30
- Metal enclosure, for lower EMI
- 2-wire interface with integrated Digital Diagnostic monitoring
- Specifications compliant with SFF 8472
- Compliant with SFP+ MSA with LC connector
- Single 3.3V power supply
- Power dissipation < 1.4 W
- Case operating temperature range: Commercial: 0°C to +70°C

  Industrial: -40°C to +85°C

Applications

- 25G Ethernet
- CPRI 10

Standard

- Compliant to SFF-8431
- Compliant to SFF 8472
- Compliant to IEEE 802.3 CC
- RoHS Compliant.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Storage Temperature	Ts	-40	-	85	ºC	
Relative Humidity	RH	5	-	95	%	
Power Supply Voltage	VCC	-0.3	-	4	V	

Recommended Operating Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Case Operating Temperature	Tcase	0		70	ºC	Commercial
		-40		85	ºC	Industrial
Power Supply Voltage	VCC	3.14	3.3	3.47	V	
Power Supply Current	ICC	-		360	mA	Commercial
				430	mA	Industrial
Data Rate	BR	24.3	25.78	26.5	Gbps	
Transmission Distance	TD		-	30	km	
Coupled fiber		Single mode fiber				9/125um SMF

Optical Characteristics

Parameter	Symbol	Min	Typ	Max	Unit	NOTE
Transmitter						
Output Opt. Power	POUT	0		5	dBm	1
Optical Wavelength	λ	1260	1270	1280	nm	SFP28-BIDI-12-30
		1320	1330	1340	nm	SFP28-BIDI-13-30
Spectral Width (-20dB)	σ			1	nm	
Optical Extinction Ratio	ER	3.5			dB	
Receiver						
Rx Sensitivity	RSENS			-18	dBm	2
Input Saturation Power (Overload)	Psat	-5			dBm	
Wavelength Range	λ	1320	1330	1340	nm	SFP28-BIDI-12-30
		1260	1270	1280	nm	SFP28-BIDI-13-30
LOS De -Assert	LOSD			-19	dBm	
LOS Assert	LOSA	-30			dBm	
LOS Hysteresis		0.5			dB	

Notes:

1. Class 1 Laser Safety per FDA/CDRH and IEC-825-1 regulations.
2. Measured with a PRBS 2^{31} -1 test pattern, @25.78Gb/s, BER@5*10-5.

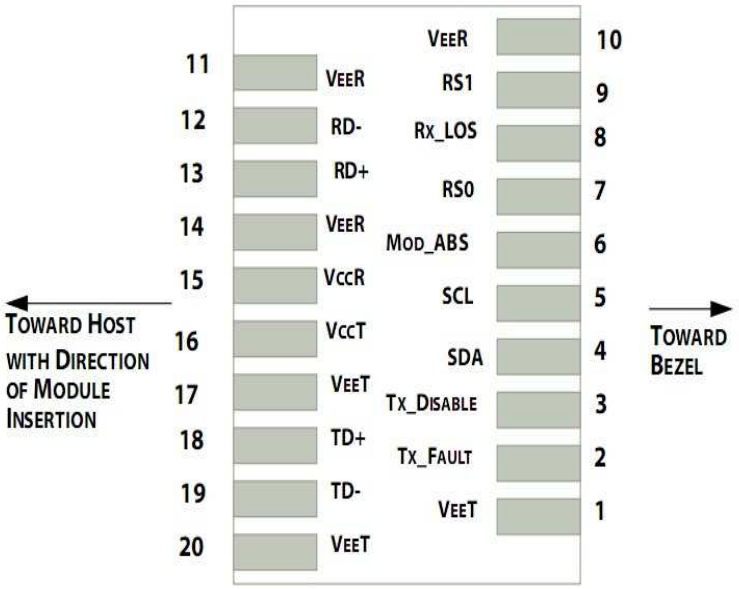
Electrical Characteristics

Parameter	Symbol	Min	Typ	Max	Unit	NOTE
Supply Voltage	Vcc	3.14	3.3	3.46	V	
Supply Current	Icc			360	mA	Commercial
				430	mA	Industrial
Transmitter						
Input differential impedance	Rin		100		Ω	1
Single ended data input swing	Vin,pp	180		800	mV	
Transmit Disable Voltage	VD	Vcc–1.3		Vcc	V	
Transmit Enable Voltage	VEN	Vee		Vee+ 0.8	V	2
Receiver						
Differential data output swing	Vout,pp	300		850	mV	3
LOS Fault	VLOS fault	Vcc–1.3		VccHOST	V	4
LOS Normal	VLOS norm	Vee		Vee+0.8	V	4

Notes:

1. Connected directly to TX data input pins. AC coupled thereafter.
2. Or open circuit.
3. Into 100 ohms differential termination.
4. Loss Of Signal is LVTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.

Pin Assignment



Pin out of Connector Block on Host Board

Pin	Symbol	Name/Description	NOTE
1	VeeT	Transmitter Ground (Common with Receiver Ground)	1
2	TX Fault	Transmitter Fault.	2
3	TX Disable	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line	4
5	SCL	2-wire Serial Interface Clock Line	4
6	MOD_ABS	Module Absent. Grounded within the module	4
7	RS0	Rate Select 0	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	6
9	RS1	No connection required	1
10	VeeR	Receiver Ground (Common with Transmitter Ground)	1
11	VeeR	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	VeeR	Receiver Ground (Common with Transmitter Ground)	1
15	VccR	Receiver Power Supply	
16	VccT	Transmitter Power Supply	
17	VeeT	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	VeeT	Transmitter Ground (Common with Receiver Ground)	1

Notes:

1. Circuit ground is internally isolated from chassis ground.
2. TX Fault is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to Vcc + 0.3V.A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm threshold. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
3. Laser output disabled on TX Disable >2.0V or open, enabled on TX Disable <0.8V. Should be pulled up with 4.7kΩ- 10kΩ host board to a voltage between 2.0V and 3.6V. MOD_ABS pulls line low to indicate module is plugged in.
4. Internally pulled down per SFF-8431 Rev 4.1.
5. LOS is open collector output. It should be pulled up with 4.7kΩ – 10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

Digital Diagnostic Functions

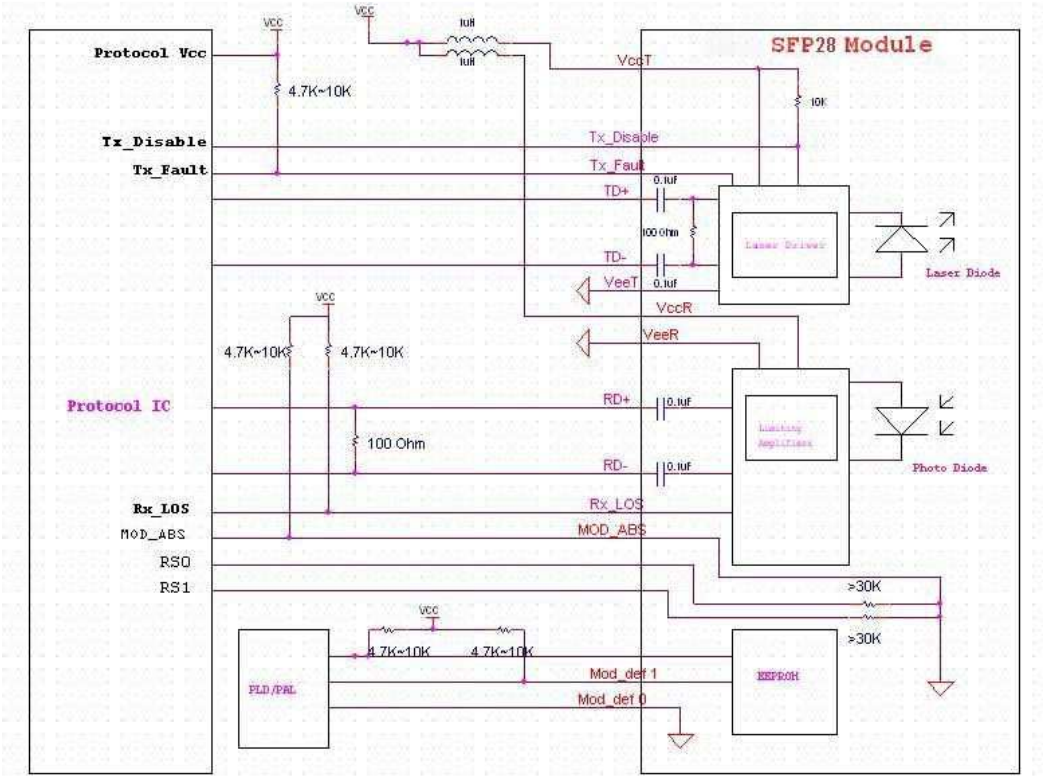
SFP28-BIDI-xx-30 transceivers support the 2-wire serial communication protocol as defined in the SFP28 MSA. The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, SFP28 transceivers provide a unique enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

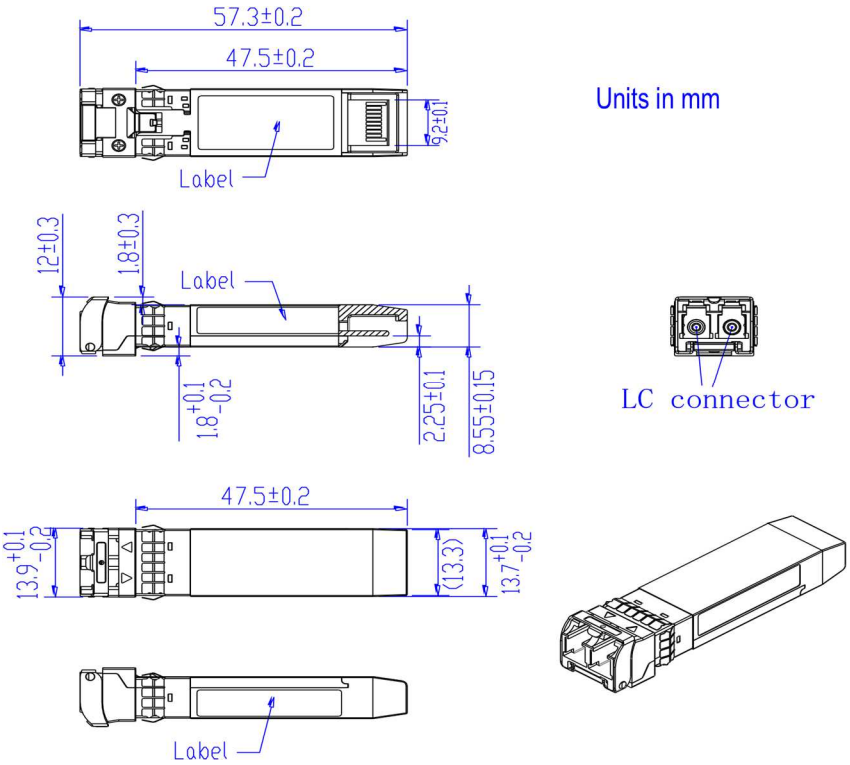
The SFP MSA defines a 256-byte memory map in EEPROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h).The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E2PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

Host - Transceiver Interface Block Diagram



Outline Dimensions



Ordering information

Part Number	Product Description
SFP28-BIDI-12-30	T1270/R1330nm, 25Gbps, LC, WDM 30km, 0°C~+70°C, With DDM
SFP28-BIDI-13-30	T1330/R1270nm, 25Gbps, LC, WDM 30km, 0°C~+70°C, With DDM
SFP28-BIDI-12-30I	T1270/R1330nm, 25Gbps, LC, WDM 30km, -40°C~+85°C, With DDM
SFP28-BIDI-13-30I	T1330/R1270nm, 25Gbps, LC, WDM 30km, -40°C~+85°C, With DDM

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Edition JUL 20, 2020
Published by Optone Technology Limited